



ASA-936-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Y. KAMIGAKI et al

Serial No. 10/663,779

Group Art Unit: 2926

Filed: September 17, 2003

Examiner: K. Quinto

For: SEMICONDUCTOR INTEGRATED CIRCUIT

**SECOND REQUEST TO CORRECT NOTICE OF ALLOWANCE**

Commissioner for Patents  
Mail Stop AF  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please correct the Notice of Allowance mailed December 20, 2004, as follows.

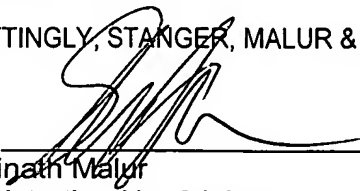
In the title, SEMICONDUCTOR INTEGRATED CIRCUIT HAVING TWO  
SWITCH TRANSISTORS FORMED BETWEEN TWO DIFFUSION-LAYER LINES.

This amendment to the title was filed on September 24, 2004, in response to a request from the Examiner in the Office Action mailed March 24, 2004.

The undersigned requests that a corrected Notice of Allowance be issued.

Respectfully submitted,

MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.

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Date: March 17, 2005